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Applicant: Ko, *et al.* Docket No.: TSM03-0615
Serial No.: 10/729,095 Art Unit: 2811
Filed: December 5, 2003 Examiner: Vu, Hung K.
For: Structure and Method of a Strained Channel Transistor and a Second
Semiconductor Component in an Integrated Circuit

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INFORMATION DISCLOSURE STATEMENT

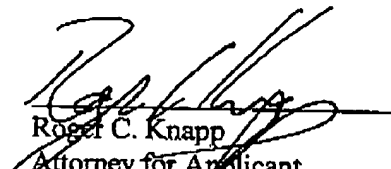
Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed form PTO/SB/08A that may be considered material to the examination of the above-identified application. Pursuant to 37 CFR 1.98(a)(2)(i), as amended, copies of U.S. Patents cited are not being submitted. However, Applicant has included copies of any non-patent literature.

No fee is due at this time, as this Information Disclosure Statement is being filed pursuant to 37 C.F.R. § 1.97(b)(3), before the mailing of a first Office action on the merits.

Respectfully submitted,

April 21, 2005

Date


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				Application Number	10/729,095
				Filing Date	December 5, 2003
				First Named Inventor	Ko, et al.
				Art Unit	2811
				Examiner Name	Vu, Hung K.
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Sheet	1	of	1		

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No.	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
	1	US-5,155,571	10-13-1992	Wang, et al.	
	2	US-5,273,815	12-28-1993	Hwang, et al.	
	3	US-5,338,980	08-18-1994	Beasom	
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	17	US-6,525,403 B2	02-25-2003	Inaba, et al.	
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[illegible]

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